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|  | KUMARAGURU COLLEGE OF TECHNOLOGY DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING 23rd Board of Studies Meeting | Ref No. KCT/ECE/BOS23/2023-24/01 Prepared by: Dr.B.Gopinath, ASP Issued by: Dr. M. Bharathi, HOD Issue Date: 15 DEC 2023 |
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|-------|--------------------|-------|--------------------------|-------|----------------------------|
| Date: | 14 DEC 2023 | Time: | 10.00 to 11.00 AM | Venue | Online via MS Teams |
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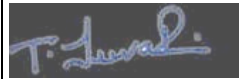
Participants of the meeting


External Panel Members

1. Dr. I. S. Akila, (Anna University Nominee), Associate Professor, Department of ECE, Coimbatore Institute of Technology, Coimbatore-641014
2. Dr. T. Veerakumar, (Subject Expert), Associate Professor, Department of ECE, National Institute of Technology Goa, Ponda, Goa-403401
3. Dr. D. Jude Hemanth, (Subject Expert), Professor, Department of ECE, Karunya Institute of Technology and Sciences, Coimbatore-641114
4. Mr. R. Sanjaykumar, (Industry Expert), Program Manager, Sasken Technologies Limited, Bengaluru-560071
5. Ms. T. Jeevaranjani, (Alumni Member), System Architect, Bosch Global Software Technologies, Coimbatore-641035

Internal Panel Members

1. Dr.M.Bharathi (BoS Chairperson)- Professor and Head, ECE, KCT
2. Dr.A.Vasuki, Professor, ECE, KCT
3. Dr.K.Kavitha (UG Programme Coordinator)-Professor, ECE, KCT
4. Dr.B.Gopinath (BoS Coordinator)-Associate Professor, ECE, KCT
5. Dr.V.Vanitha (Member from allied department)-Professor, IT, KCT
6. Dr.A.Amsaveni-Professor, ECE, KCT
7. Dr.S.Umamaheswari-Associate Professor, ECE, KCT
8. Dr.S.Sasikala-Associate Professor, ECE, KCT
9. Dr.M.Alagumeenaakshi-Associate Professor, ECE, KCT
10. Dr. J. Mohankumar - Associate Professor, ECE, KCT
11. Dr.K.Thilagavathi-Assistant Professor, ECE, KCT






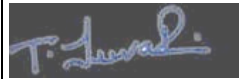
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12. Ms.R.DhivyaPraba-Assistant Professor, ECE, KCT
13. Ms.A.Kalaiselvi-Assistant Professor, ECE, KCT
14. Mr.D.Allin Joe-Assistant Professor, ECE, KCT
15. Ms.K.Karthika-Assistant Professor, ECE, KCT
16. Dr.V.P.Ajay-Assistant Professor, ECE, KCT
17. Mr.S.Prasanna Kumaar, Assistant Professor, ECE, KCT
18. Dr.J.Boobalan, Assistant Professor, ECE, KCT
19. Dr.D.Gracia, Assistant Professor, ECE, KCT
20. Ms.T.Jaspar Vinitha Sundari-Assistant Professor, ECE, KCT
21. Ms.S.Meenakshi, Assistant Professor, ECE, KCT
22. Ms.S.S. Aishwarya Lakshmi - Student Representative, ECE, KCT
23. Ms. L.Monisha –Student Representative, ECE, KCT

Agenda:

1. Action Taken Report on previous BoS meeting
2. CO / PO attainments of 2019-23 batch
3. List of Value Added Courses (VACs) for the AY 2023-24
4. Introduction of Edge Courses
5. Introduction of Honours Courses
6. Introduction of curriculum 2024 – Generic Table
7. Inputs for curriculum development for the upcoming regulation syllabus
8. Approval of Panel of Examiners for QP Setting / Lab Exams for the AY 2023-24
9. Any other matters (with the permission of Chairperson, BoS)






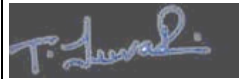
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MINUTES OF MEETING & ACTION POINTS






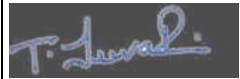
Codes: A: Action C: Comment D: Decision R: Recommendation


| S.No | CODE | DESCRIPTION & DISCUSSION POINTS | | | | | | | | | | | | | | | |
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| 1. | A | <ul style="list-style-type: none"> Dr. M. Bharathi, HoD-ECE, delivered the welcome address. Vision, mission, PEO, PO and PSO of the UG Programme were shared with the panel members. The significant highlights of the department were also presented. <p>Action Taken Report on Previous BoS Meeting</p> <ul style="list-style-type: none"> The Action Taken Report of the previous 22nd BoS meeting was presented, and all the suggestions given by the BoS members were incorporated in the undergraduate R18 syllabus. It was also assured that major changes would be incorporated in the next revision of the curriculum and syllabus. | | | | | | | | | | | | | | | |
| | | <table border="1"> <thead> <tr> <th>S. No</th> <th>Feedback</th> <th>Action Taken</th> </tr> </thead> <tbody> <tr> <td>1.</td> <td>Dr. C. Vasanthanayaki suggested to add a few case studies related to microcontroller applications.</td> <td>Case studies have been added in the course U18ECI6204 - Microcontroller and its Applications</td> </tr> <tr> <td>2.</td> <td>Dr. M. Sudhakar suggested to include the content related to architectures of general purpose and advanced processors.</td> <td>General purpose and advanced processors have been added in the course U18ECI5206 - Embedded Processor Architecture</td> </tr> <tr> <td>3.</td> <td>Mr. S. Chellakumar suggested to include the contents related to FINFET technology in VLSI related courses.</td> <td>The major revisions in the curriculum and syllabus will be carried out during the next revision.</td> </tr> <tr> <td>4.</td> <td>Dr. C. Vasanthanayaki suggested to include Mini Project along with the lab components.</td> <td>Project component and lab components have been added in the course U18ECE0058 - Advanced HMI. Based on the industry need, 50% of the content is taught as practical course.</td> </tr> </tbody> </table> | S. No | Feedback | Action Taken | 1. | Dr. C. Vasanthanayaki suggested to add a few case studies related to microcontroller applications. | Case studies have been added in the course U18ECI6204 - Microcontroller and its Applications | 2. | Dr. M. Sudhakar suggested to include the content related to architectures of general purpose and advanced processors. | General purpose and advanced processors have been added in the course U18ECI5206 - Embedded Processor Architecture | 3. | Mr. S. Chellakumar suggested to include the contents related to FINFET technology in VLSI related courses. | The major revisions in the curriculum and syllabus will be carried out during the next revision. | 4. | Dr. C. Vasanthanayaki suggested to include Mini Project along with the lab components. | Project component and lab components have been added in the course U18ECE0058 - Advanced HMI. Based on the industry need, 50% of the content is taught as practical course. |
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| 4. | Dr. C. Vasanthanayaki suggested to include Mini Project along with the lab components. | Project component and lab components have been added in the course U18ECE0058 - Advanced HMI. Based on the industry need, 50% of the content is taught as practical course. | | | | | | | | | | | | | | | |
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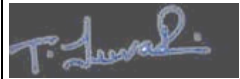
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| | | 5. The BoS panel members recommended to include a few industry-based elective courses. | The following list of industry-based courses was explained to the BoS panel members. U18ECE0055 - Automotive Electronics U18ECE0056 - Multicore Architecture and Programming for Embedded Systems U18ECE0057 - Introduction to HMI U18ECE0058 - Advanced HMI & Value Added Courses (VACs) |
| 2. | C, R | CO / PO attainments of 2019-23 batch <ul style="list-style-type: none"> The CO, PO and PSO attainments for the 2023 passed out batch students were summarized by the BoS coordinator Dr.B.Gopinath. Dr. D. Jude Hemanth, subject expert, asked about the fixing the target levels for various courses and explanation was given by the chairperson. He suggested to reframe the PEO1 statement by including Electronics in the place of VLSI Systems so that it could help to reach the attainment targets. | |
| 3. | R | List of Value Added Courses (VACs) for the AY 2023-24 <ul style="list-style-type: none"> The following list of Value Added Courses (VACs) for the academic year 2023-24 was presented. The contents of the courses were developed and handled by the industry experts which could enhance the modern tool usage ability and teamwork practice among students. <p>U18ECV2301 – Nano Satellite and Communication Systems. U18ECV2302 – Hands-On Training on LoRaWAN U18ECV2303 – PCB Design Techniques: From Fundamentals to Gerber file mastery U18ECV2304 – Problem Solving and Algorithmic Thinking with C++ U18ECV2305 – Emerging trends in IoT: From Edge Computing to AI Integration U18ECV2306 – Entrepreneurship and Innovation Resources</p> | |

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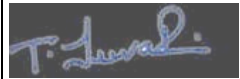
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| 4. | D | Introduction of Edge Courses and Honours Track <ul style="list-style-type: none"> The following list of Edge courses (2 Credits) for 2023 batch students was presented and approved by the BoS panel members. The edge courses will be handled by Industry Experts. <ul style="list-style-type: none"> U18ECR0001 - Quantum Computing U18ECR0002 - Machine Learning Algorithms and Practices U18ECR0003 - Mobile Device Security U18ECR0004 - Forensic Audio-Video Analysis U18ECR0005 - Digital Image Processing and Biometrics The following list of Honours courses, to be introduced from 2022 batch students with 18 credits, was presented and approved by the BoS panel members. These courses were designed on VLSI Design vertical which will be thought from IV Semester onwards by internal faculty members. <ul style="list-style-type: none"> Introduction to VLSI Design and Data Structures High Level Digital Design Logic Synthesis CMOS VLSI Design System Verilog and Verification / Low Power VLSI / System on Chip with FPGA Bus Protocol Design / Advanced VLSI Design Dr. D. Jude Hemanth suggested to include one or two NPTEL courses under credit transfer and a project component instead of all theory courses. |
| 5. | C, R | Introduction of curriculum 2024 – Generic Table <ul style="list-style-type: none"> A draft version of curriculum components under regulation 2024 was presented. Dr. I. S. Akila, Anna university nominee, suggested to divide the entire curriculum components as category wise as per the AICTE and University guidelines such as Basic Science, Engineering Science, Professional Core and so on. The chairperson assured to present the same during the next BoS meeting, since the curriculum design process is in the initial stage. Dr. D. Jude Hemanth recommended to design the courses as per the guidelines of National Education Policy (NEP). |

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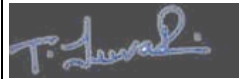
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| 6. | C, R | <p>Inputs for curriculum development for the upcoming regulation syllabus</p> <ul style="list-style-type: none"> The new curriculum for the upcoming regulation 2024 was presented semester wise and inputs were received from the BoS members. Dr. I. S. Akila suggested to have a clarity in deciding the content of Design Thinking course in the first semester. She also insisted to maintain the AICTE guidelines in deciding the number of credits under Humanities and Social Sciences (HS) category. Dr. T. Veerakumar, Subject Expert, suggested to combine Fundamentals of Technical Writing course and Language Elective I as a single course for 4 credits in the first semester. It was proposed to swap Circuit Theory course from third semester to second semester in the slot of Electrical Drives and Control, based on the feedback received from service department. Mr. R. Sanjaykumar, (Industry Expert), and other BoS members suggested to include Python Programming Language course in the first year along with the C Programming Language course. Dr. I. S. Akila and Ms. T. Jeevaranjani, (Alumni Member) welcomed the inclusion of Object Oriented Programming and Data Structure course in third semester. Mr. R. Sanjaykumar suggested to include laboratory component for the course. However, Dr. T. Veerakumar suggested to shift the course to higher semesters to avoid the overloading on students. Dr. T. Veerakumar insisted to introduce laboratory component for Signals and Systems course if there is a possibility. The chairperson replied that the experiments related to the course will be conducted in Digital Signal Processing course in the fourth semester. The expert members of Department Advisory Board (DAB) suggested to combine the content of three courses Electromagnetic Fields, Transmission Lines and Waveguides and Antenna and Propagation into two courses so that a three credit slot could be used for other courses. The suggestion was accepted by the BoS panel members also. |
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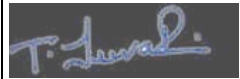
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| | <ul style="list-style-type: none"> • Dr. T. Veerakumar suggested to introduce Linear Integrated Circuits Course after Analog Electronic Circuits course. • Ms. T. Jeevaranjani suggested the usage of MATLAB tool in Control System and other related courses as per the industry needs. Dr. T. Veerakumar advised to use the modern tools and programming language as needed. • Dr. I. S. Akila and Dr. T. Veerakumar recommended to introduce the communication related courses from fourth semester onwards instead of fifth semester. • Also, Ms. T. Jeevaranjani, Dr. I. S. Akila and Dr. T. Veerakumar recommended to shift Machine Learning course to sixth semester before Project Phase and bring all the possible core courses in the third year curriculum in line with GATE syllabus. • Mr. R. Sanjaykumar and Ms. T. Jeevaranjani recommended to teach Machine Learning course with as an application oriented course with laboratory component. • Dr. I. S. Akila suggested to use suitable course names for Embedded Processor Architecture and Microcontroller and Applications. Similarly, the panel members recommended to use exact course names for Communication Engineering I and Communication Engineering II as per their contents. • Panel members discussed about combining Communication Engineering I & II courses and concluded that there was no possibility for the same because of the contents of analog and digital communication components. • It was proposed to shift the seventh semester courses to eighth semester and Project Phase II to seventh semester so that students could complete their project work well in advance before their internships and placements. • Dr. I. S. Akila and Dr. T. Veerakumar suggested to shift a part of the courses, probably elective courses to eight semester and keep the core courses in the seventh semester. • Dr. T. Veerakumar also suggested to teach the courses in fast track before sending students to internships. |
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| | | <ul style="list-style-type: none"> • Dr. I. S. Akila and Mr. R. Sanjaykumar recommended to shift Project Phase I to sixth semester and distribute the final year courses to seventh and eight semesters so that students will get preparation time for their placement drives. • Dr. D. Jude Hemanth advised to do the shifting of seventh and eight semester courses based on students' placement and higher studies. He also recommended to have few more courses related to Python and Java programming. • Dr.K.Kavitha, Professor, ECE accepted and recommended to have the core courses with laboratory components in seventh semester. • The chairperson and Dr.K.Kavitha summarized various verticals of elective courses. Since the curriculum design process is in its early stage it was concluded that the finalizing elective verticals and courses would be discussed in forthcoming BoS meetings. • Dr. D. Jude Hemanth recommended to have only three major elective verticals, namely VLSI and associated hardware, Communication and AI & ML. He also suggested to introduce few project based courses along with regular courses. |
| 7. | D, R | Approval of Panel of Examiners for QP Setting / Lab Exams for the AY 2023-24 <ul style="list-style-type: none"> • A list of panel of external examiners for the end semester exam question paper setting and project viva-voce for the academic year 2023-24 was presented and approved by the BoS members. |
| 8. | R | Any other matters <ul style="list-style-type: none"> • Dr.M.Bharathi, BoS-Chairperson, delivered the vote of thanks and the 23rd BoS meeting was ended. |

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