



ELECTRICAL CLUSTER & PHYSICS DEPARTMENT ORGANIZES

INDUSTRY ACADEMIA RESEARCH CONCLAVE 2025

on

Silicon Synergy: Shaping Semiconductor Future in Empowering the India Semiconductor Mission

CONCLAVE SCHEDULE:

Date	Time	Event Description	Resource Person details
31.01.2025	9.30 AM - 10.30 AM	Inauguration function & Key note session	Mr. Sriram -K Vasudevan, Lead-Technical Evangelist, Asia Pacific & Japan Region, Software and advanced Tech group, Intel corporation, Chennai, Tamilnadu.
	10:30 AM - 10:45 AM	Break	
	10.45AM- 11.45 AM	Tech Talk 1 VLSI hardware for AI and ML	Mr. Sriram - K Vasudevan, Lead-Technical Evangelist, Asia Pacific & Japan Region, Software and advanced Tech group, Intel corporation, Chennai, Tamilnadu.
	11.45 AM - 01.00PM	Tech Talk 2 SoC Development Flow - Opportunities	Mr. K. Manivannan, Program manager, Qualcomm, Bengaluru, Karnataka.
	01.00 PM- 1.45PM	Lunch	
	01.45 PM - 03.00 PM	Tech Talk 3 ASIC overview & Career Path steps	Mr. S. Chellakumar, CAD Principal engineer, Enphase Solar energy Pvt. Ltd., Bengaluru, Karnataka.
	03.00 PM - 03.15 PM	Break	
	03.15 PM - 04.30 PM	Panel 1 "Bridging the Skills Gap: Preparing Students for the Semiconductor Revolution"	Chaired by: Rear Admiral Mohandoss V (Retd), Director Defense Pgm KC.IRI, Kumaraguru College of Technology, Coimbatore, Tamilnadu Panel Members: 1.Mr. S. Chellakumar, CAD Principal engineer, Enphase Solar energy Pvt Ltd, Bengaluru, Karnataka 2.Dr. E.R. Rajkumar, Principal Project Manager, Cadence Design Systems (India) Pvt. Ltd., Bengaluru, Karnataka. 3.Mr. K. Manivannan, Program manager, Qualcomm, Bengaluru, Karnataka. 4.Mr. K. S. Sarath, Sr Engineer (RTL), VVDN Technologies Ernakulam, Kerala, India

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01.02.2025	9.00 AM - 10.45 AM	Tech Talk 1 Electronics Industry in AI Era	Ms. B. Nandhini, Director, TESCOM, Bengaluru, Karnataka.
	10.45 AM - 11.00 AM	Break	
	11.00 AM - 12.45 PM	Tech Talk 2 IC design process for Standards based communication protocol: Bluetooth	Mr. M. Rajanikanth, General Manager, HW Systems, Embedded SW and Semiconductors, TenXer Labs, Bengaluru, Karnataka.
	12.45 PM- 01.45 PM	Lunch	
	01.45 PM - 03.00 PM	Tech Talk 3 Leveraging EDA Tools for Efficient VLSI Design: A TCAD Case Study	Ms. Sangeeta Mangesh, Manager (Edutech Program) Cadre Design Systems, Ghaziabad, Uttar Pradesh.
	03.00 PM - 03.15 PM	Break	
	03.15 PM - 04.30 PM	Panel 2 "Building India's Semiconductor Ecosystem: Unlocking the Potential of the National Semiconductor Mission"	Chaired by: Ms. Sangeetha Mangesh, Manager (Edutech Program) Cadre design systems, Ghaziabad, Uttar Pradesh. Panel Members: 1. Mr. M. Rajanikanth, General Manager, HW Systems, Embedded SW and Semiconductors, TenXer Labs, Bengaluru, Karnataka. 2. Mr. Maheshchandra, Senior Manager, Capgemini Pvt. Ltd., Coimbatore, Tamilnadu 3. Mr. S. Ganesh Prabhu, Embedded Engineer (Firmware), Calibre Interconnects, Coimbatore, Tamil Nadu. 4. Mr. L. Gunasekar Senior Manager, Capgemini Pvt. Ltd., Coimbatore, TamilNadu
	04.30 PM - 05.00 PM	Valedictory	